

BALASORE SCHOOL OF ENGINEERING, BALASORE

LESSION PLAN FOR 3RD SEMESTER, SESSION:-2022-23(W-22)

BRANCH:- Computer Science & Engineering				SEMESTER:-3RD		
SUBJECT:- COMPUTER SYSTEM ARCHITECTURE				THEORY:-TH-1		
NAME OF TEACHER:- SUVENDU NAYAK						
SL. No.	Month /No .of academic days available for the subject	Chapter	DATE	TOPICS TO BE COVERED	No of periods available as per syllabus	No of periods available as per plan
	SEPT-2022 (10)	Unit-1	16/9/22	1. Basic structure of computer hardware 1.1Basic Structure of computer hardware	10	10
			19/9/22	1.2Functional Units		
			20/9/22	1.3Computer components		
			21/9/22	1.4 Performance measures		
			23/9/22	1.5 Memory addressing & Operations		
		Unit-2	24/9/22	2. Instructions & instruction Sequencing 2.1 Fundamentals to instructions		
			26/9/22	2.2 Operands		
			27/9/22	2.3 Op Codes		
			28/9/22	2.4 Instruction formats		
			30/9/22	2.5 Addressing Modes		

SL. No.	Month /No .of academic days available for the subject	Chapter	DATE	TOPICS TO BE COVERED	No of periods available as per syllabus	No of periods available as per plan	
	OCT-2022(16)		01/10/22	2.5 Addressing Modes Cmplt..	20	16	
			10/10/22	REVISION			
		Unit-3		11/10/22			3. Processor System
				12/10/22			3.1 Register Files
				14/10/22			3.2 Complete instruction execution ☑ Fetch,
				15/10/22			Decode,
				17/10/22			Execution
				18/10/22			3.3 Hardware control
				19/10/22			3.4 Micro program control
				21/10/22			REVISION
		Unit-4		22/10/22			4. Memory System
				25/10/22			4.1Memory characteristics
				26/10/22			4.2 Memory hierarchy CONT.....
				28/10/22			4.2 Memory hierarchy
				29/10/22			4.3 RAM and ROM organization CONT.....
				31/10/22			4.3 RAM and ROM organization

SL. No.	Month /No .of academic days available for the subject	Chapter	DATE	TOPICS TO BE COVERED	No of periods available as per syllabus	No of periods available as per plan
P1 61-01	NOV-2022(16)	Rest of Unit-4	01/11/22	4.4 Interleaved Memory	20	16
			02/11/22	4.5 Cache memory		
			04/11/22	4.6 Virtual memory		
		05/11/22	REVISION			
		07/11/22	REVISION			
		Unit-5	09/11/22	5. Input – Output System 5.1 Input - Output Interface		
			11/11/22	5.2 Modes of Data transfer cont..		
			12/11/22	5.2 Modes of Data transfer cmplt..		
			21/11/22	5.3 Programmed I/O Transfer		
			22/11/22	5.4 Interrupt driven I/O		
			23/11/22	5.5 DMA		
			25/11/22	5.6 I/O Processor		
		Unit-6	26/11/22	6. I/O Interface & Bus architecture 6.1 Bus and System Bus Cont..		
			28/11/22	6.1 Bus and System Bus Cmplt.		
			29/11/22	6.2 Types of System Bus Data, Address, Control cont..		
			30/11/22	6.2 Types of System Bus Data, Address, Control cmplt.		

SL. No.	Month /No .of academic days available for the subject	Chapter	DATE	TOPICS TO BE COVERED	No of periods available as per syllabus	No of periods available as per plan			
	DEC-2022(8)	Rest of Unit-6	02/12/22	6.3 Bus Structure	10	8			
			03/12/22	6.4 Basic Parameters of Bus design					
			05/12/22	6.5 SCSI 6.6 USB					
		Unit-7	09/12/22	7. Parallel Processing 7.1 Parallel Processing					
			10/12/22	7.2 Linear Pipeline					
			12/12/22	7.3 Multiprocessor					
			13/12/22	7.4 Flynn"s Classification Cont..					
			14/12/22	Flynn"s Classification cmplt.					
			16/12/22	REVISION					
			17/12/22	REVISION					
			19/12/22	REVISION					
							Total Class	60	50

Brief Summary of the Plan

Sino	Month	Units/Chapter To be Covered	Percentage of Coverage
1	SEPT	Chapter- 1, 2	30 %
2	OCT	Chapter- 3, 4 - 4.3	25%
3	NOV	Chapter- 4.4 - 4.6 , 5, 6 - 6.2	30%
4	DEC	Chapter- 6.3 - 6.6, 7	15%

Suresh N Nayak
Signature of the Faculty

Date 13/09/22

Signature of the Principal

Date

Panda
13/9/22